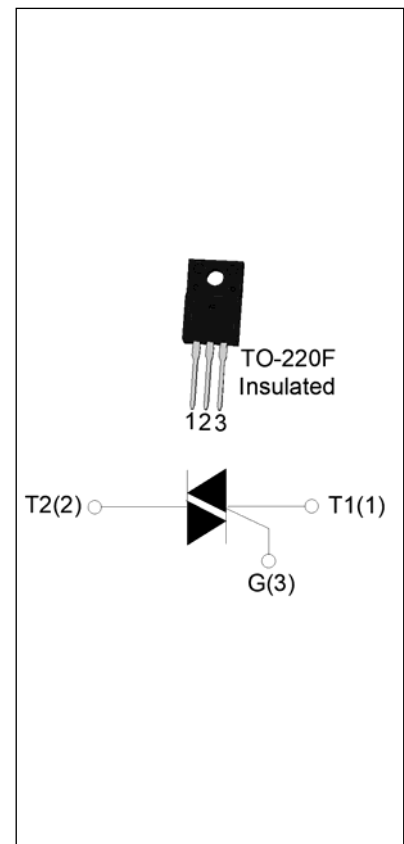


T835H-8F 8A TRIAC

Rev.A.1.1

DESCRIPTION:

The T835H-8F triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, T835H-8F provides a very high switching capability up to junction temperatures of 150°C. By using an external plastic package, T835H-8F provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	800	V
$I_{GT\ I/II/III}$	35/35/35	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-150	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c \leq 113^\circ\text{C}$)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle , $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		88	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	32	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=150^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$
Peak gate current ($t_p=20\mu\text{s}$, $T_j=150^\circ\text{C}$)	I_{GM}	4	A

Average gate power dissipation ($T_j=150^{\circ}\text{C}$)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25^{\circ}\text{C}$; non-repetitive, off-state; FIG.7)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II -III	MAX.	35	mA
V_{GT}		I - II -III	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=150^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	I - II -III	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I -III	MAX.	50	mA
		II		70	
I_H	$I_T=100\text{mA}$		MAX.	45	mA
dV/dt	$V_D=540\text{V}$ Gate Open $T_j=150^{\circ}\text{C}$		MIN.	500	V/ μs
$(dI/dt)_c$	$(dV/dt)_c=20\text{V}/\mu\text{s}$, $T_j=150^{\circ}\text{C}$		MIN.	5	A/ms
t_{on}	$I_G=40\text{mA}$ $I_A=200\text{mA}$ $I_R=20\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	3	μs
t_{off}				30	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=11\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.4	V
V_{TO}	Threshold voltage	$T_j=150^{\circ}\text{C}$	0.77	V
R_D	Dynamic resistance	$T_j=150^{\circ}\text{C}$	52.6	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=150^{\circ}\text{C}$	1.5	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	3.5	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	60	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION

T	8	35	H	-8	F
Triacs	$I_T(RMS):8A$				F:TO-220F(Ins)
	$35:I_{GT1-3} \leq 35mA$			$8:V_{DRM} / V_{RRM} \geq 800V$	
			High junction temperature		

MARKING

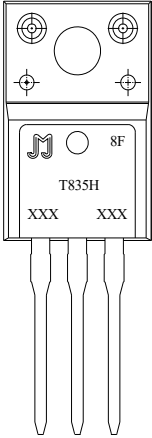
	XXX XXX Year Month Production Code
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FIG.1: Maximum power dissipation versus RMS on-state current

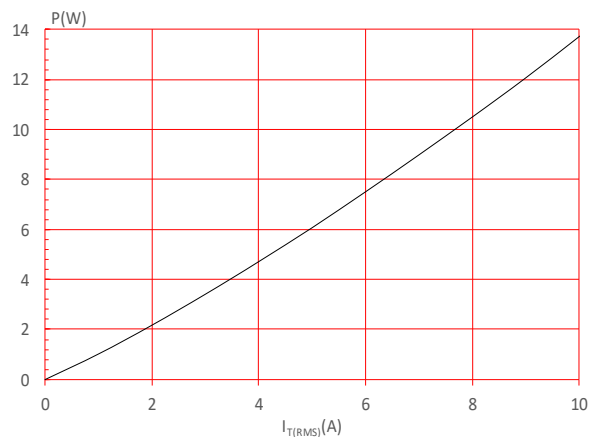


FIG.2: RMS on-state current versus case temperature

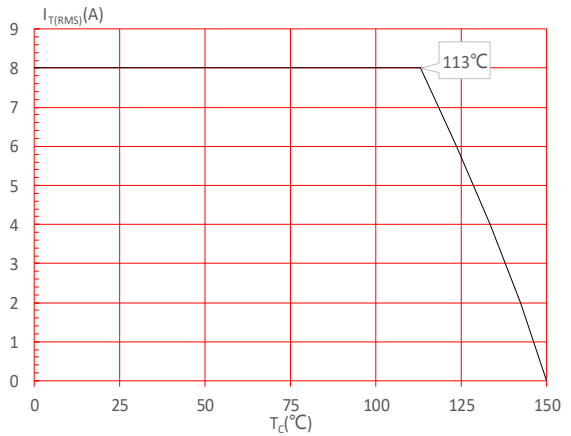


FIG.3: Surge peak on-state current versus number of cycles

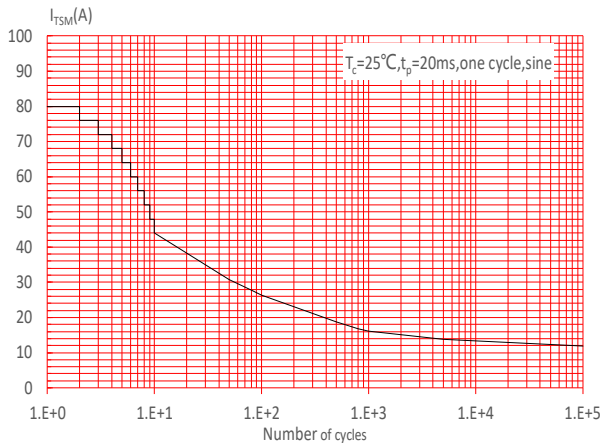


FIG.4: On-state characteristics

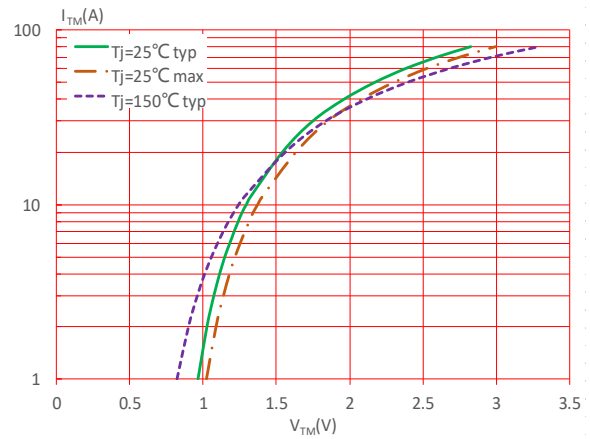


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 100\text{A}/\mu\text{s}$)

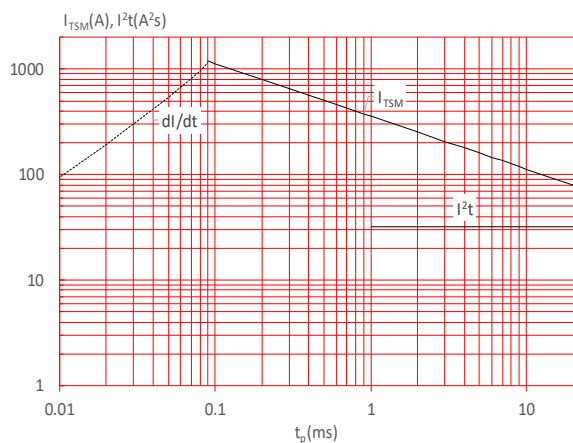


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

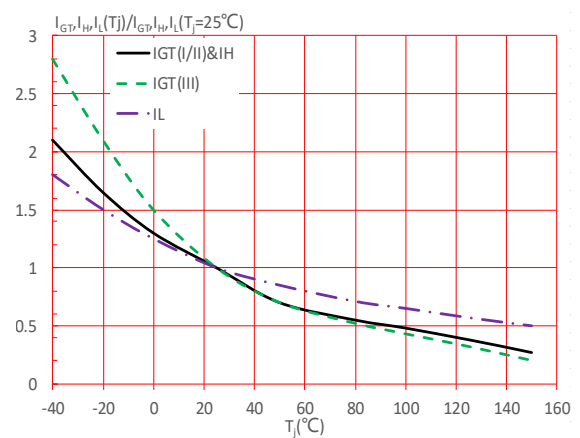
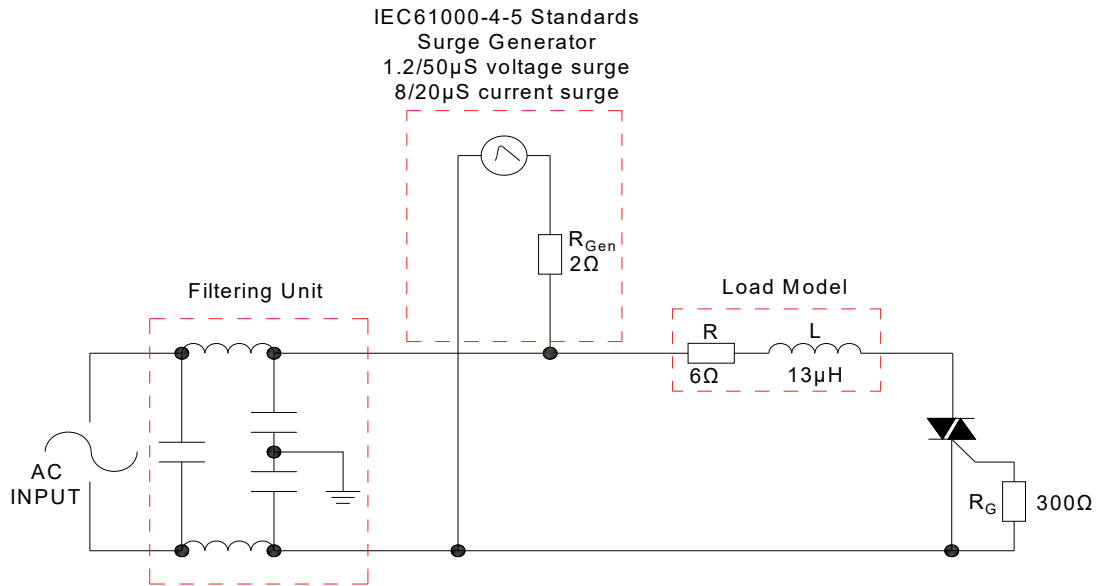


FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics.

ORDERING INFORMATION

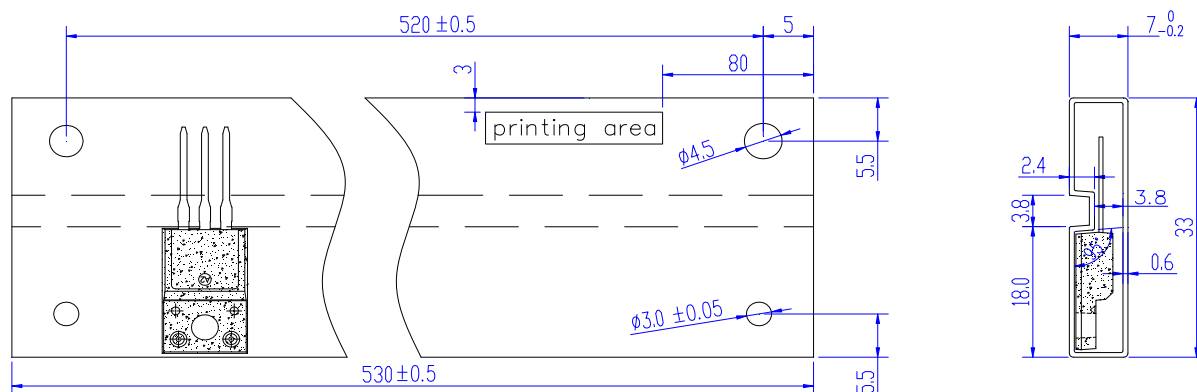
Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		I - II - III			
T835H-8F	800	35	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

Technical drawing of a 3000 series plug, showing three views: a top view, a front view, and a side view. The top view shows a rectangular base with two mounting holes and a central hole. The front view shows the plug's profile with dimensions: total height H, base thickness L1, plug length L2, and base width G. The side view shows the plug's profile with dimensions: total height H, base thickness L1, plug length L2, and base width G. The drawing includes a detail view of the mounting holes with dimensions: hole diameter $\phi 3.05-3.25\text{mm}$, hole spacing L3, and hole diameter $\phi 3.05-3.25\text{mm}$. The drawing also includes a detail view of the plug's tip with dimensions: tip diameter $\phi 3.05-3.25\text{mm}$, tip length L2, and tip diameter $\phi 3.05-3.25\text{mm}$.

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G	2.40		2.70	0.094		0.106
H	28.0		29.8	1.102		1.173
I	4.80		5.20	0.189		0.205
J	12.60		13.00	0.496		0.512
K	9.60		10.00	0.378		0.394
L1	3.20		3.80	0.126		0.150
L2	1.14		1.70	0.045		0.067
L3	3.20		3.60	0.126		0.142
M	4.80		5.20	0.189		0.205
V1		45°			45°	



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220F	TUBE	50	1,000	5,000

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